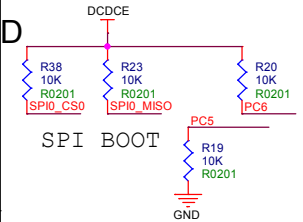


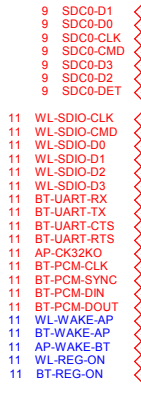
GPIO use guide:

1. Note that the voltage of SOC GPIO must matches the external IO voltage.
2. The pull up voltage of the GPIO is selected to correspond to the power field voltage of GPIO.



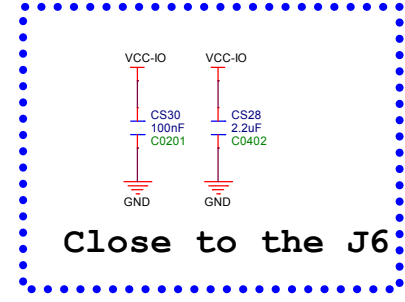
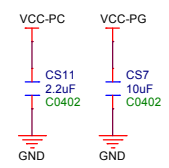
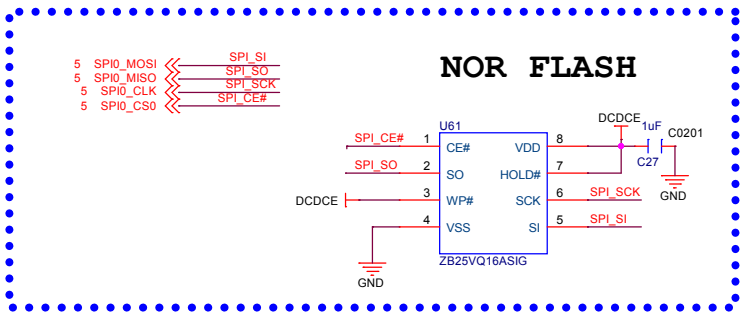
U1B		U1B	
5 SPI0_CLK	B1	PH0/UART0_TX/CAN_TX/PWM3/TWI1_SCK/PH_EINT0	D16
9 PC1	C2	PH1/UART0_RX/CAN_RX/PWM4/TWI1_SDA/PH_EINT1	D15
5 SPI0_MOSI	C1	PH2/UART5_TX/SPDIF_CLK/PWM2/TWI2_SCK/PH_EINT2/EPHY-SPD-LED	B15
5 SPI0_CS0	D1	PH3/UART5_RX/SPDIF_IN/PWM1/TWI2_SDA/PH_EINT3/EPHY-LINK-LED	C15
5 SPI0_MISO	D2	PH4/SPDIF_OUT/TWI3_SCK/PH_EINT4	A14
9 PC5	D3	PH5/UART2_TX/H_I2S3_MCLK/SP1_CS0/TWI3_SDA/PH_EINT5	B14
9 PC6	E2	PH6/UART2_RX/H_I2S3_BCLK/SP1_CLK/TWI4_SCK/PH_EINT6	C14
9 PC7	F4	PH7/UART2_RTS/H_I2S3_LRCK/SP1_MOSI/TWI4_SDA/PH_EINT7	B12
9 PC8	E3	PH8/UART2_CTS/H_I2S3_DOUT0/SP1_MISO/H_I2S3_DIN1/PH_EINT8	C13
9 PC9	F1	PH9/H_I2S3_DIN0/SP1_CS1/H_I2S3_DOUT1/PH_EINT9	A12
9 PC10	F2	PH10/IR_RX/TCON_TRIG1/PH_EINT10	B13
9 PC11	F3		
9 PWR-LED	G2		
9 STATUS-LED	G3		
9 GPIO-SCK	H1		
9 GPIO-SDA	H2		
9 USB1-DRVVBUS	H3		
	F5		
	J2		
	K1		
	K3		
	L2		
	L3		
	K2		
	A6		
	C7		
	C6		
	B5		
	C5		
	B7		
	A4		
	B4		
	C4		
	B3		
	C8		
	B10		
	C9		
	C10		
	B8		
	B9		
	A8		
	B8		
	C3		
	A2		
	B2		
	D7		

	H6		
	D4		
	G6		
	E4		
	G5		
	H4		
	H5		
	G4		
	E6		
	D5		
	L5		
	K4		
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	E5		
	L6		
	L4		
	K6		
	P15		
	J6		
	C18		
	B18		



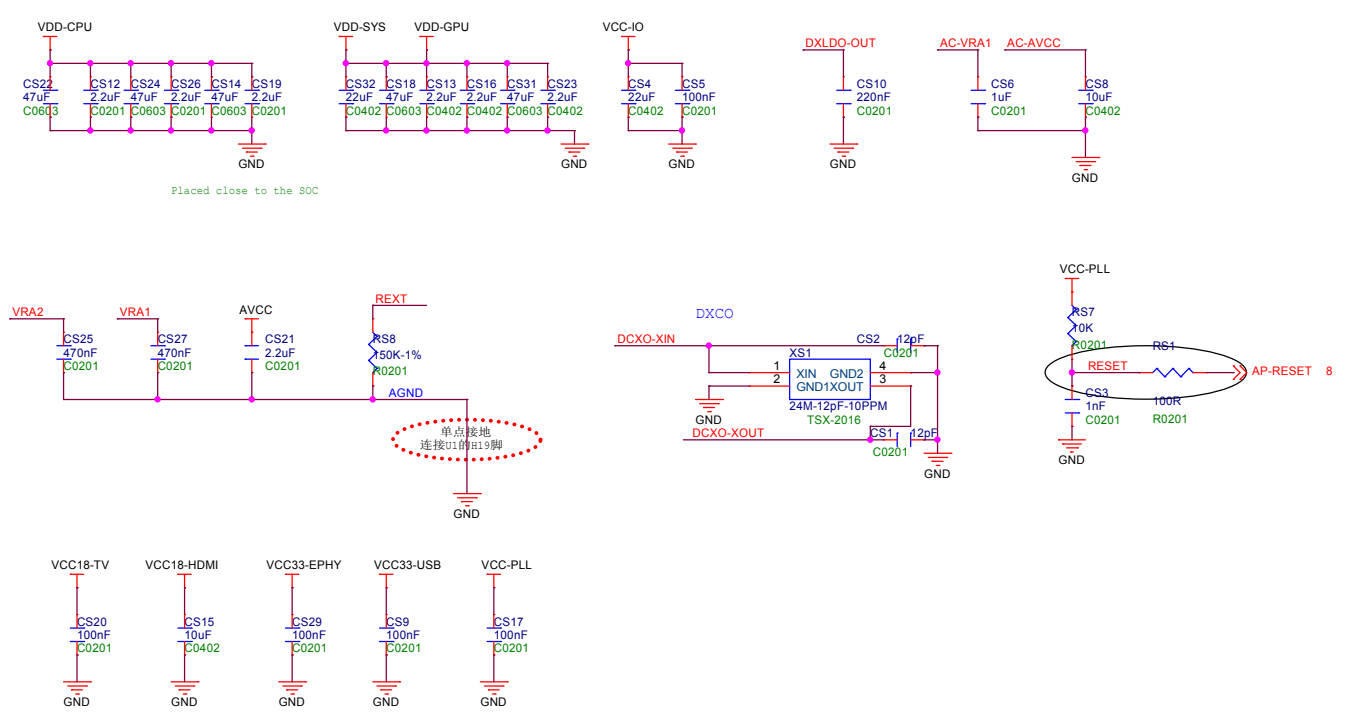
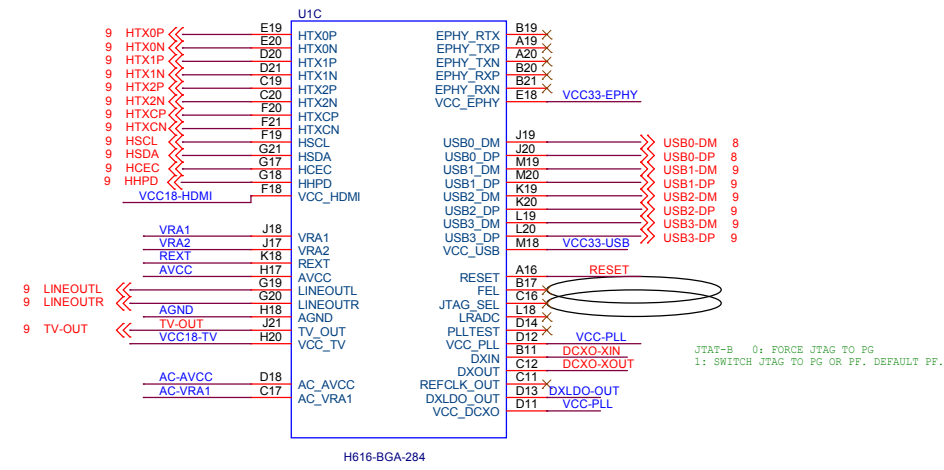
1.8/3.3V

H616-BGA-284

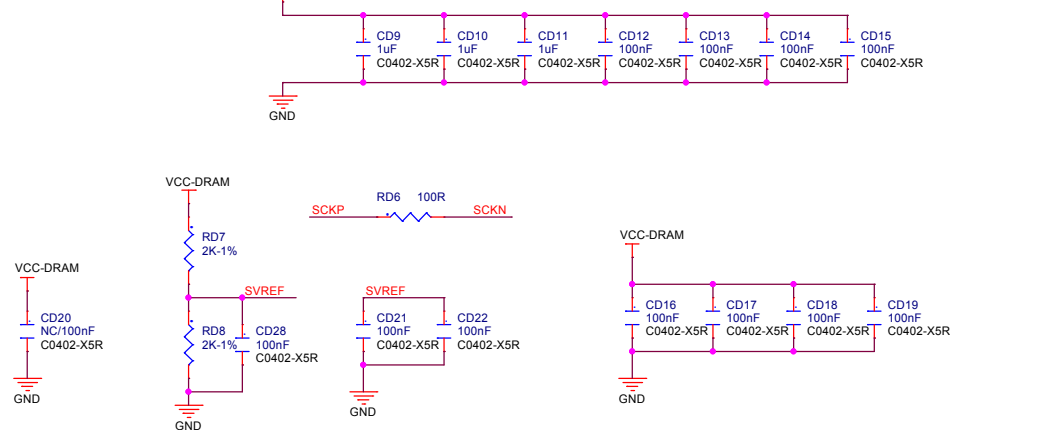
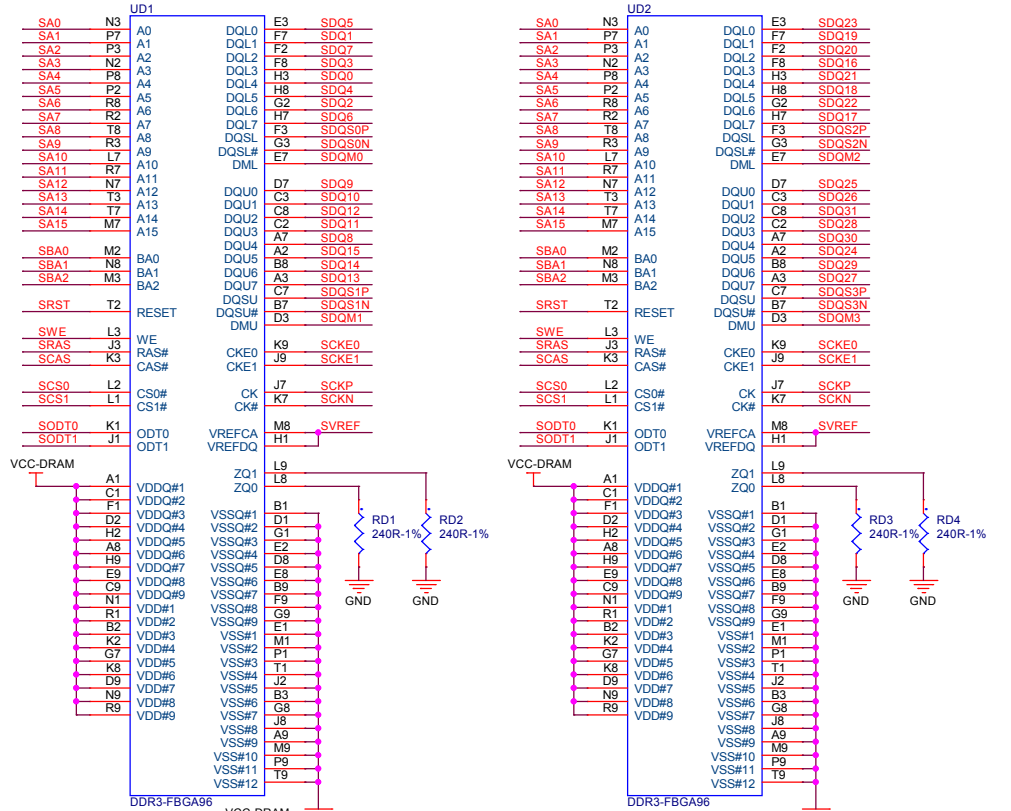
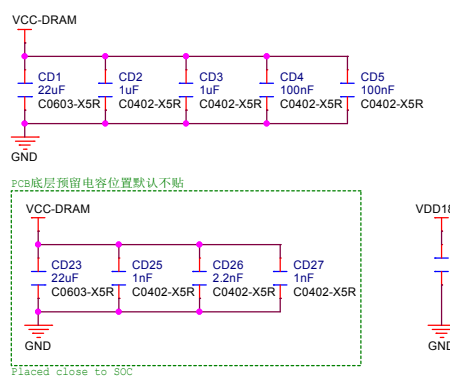
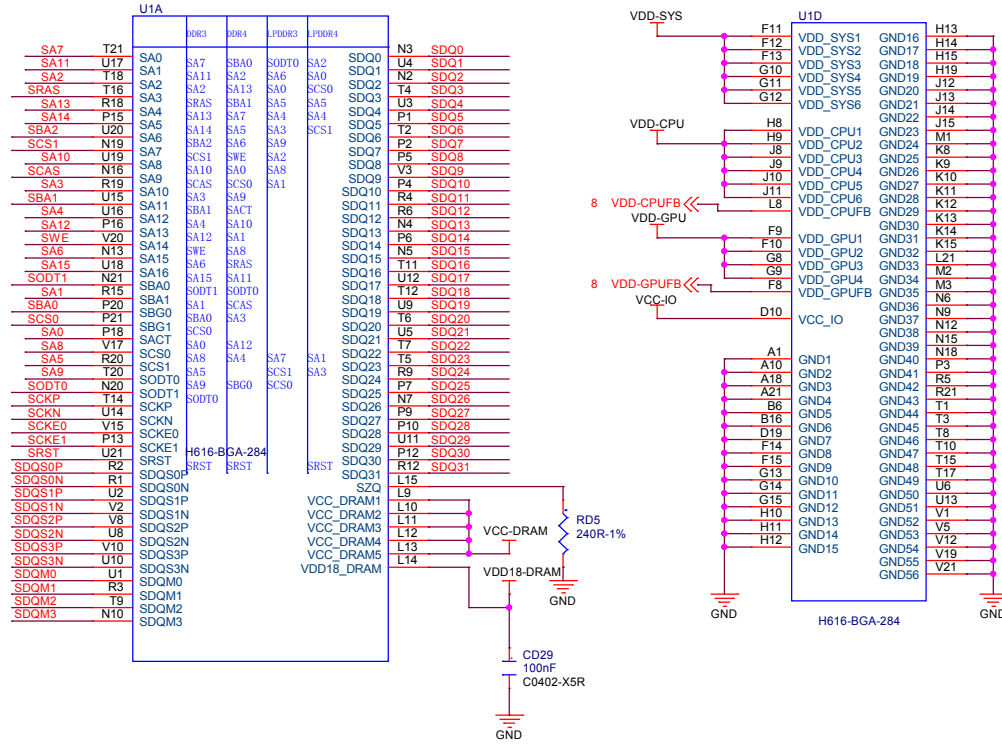


Close to the J6

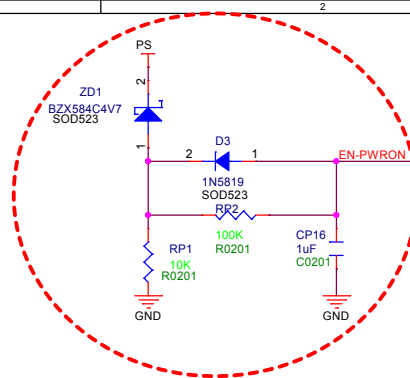
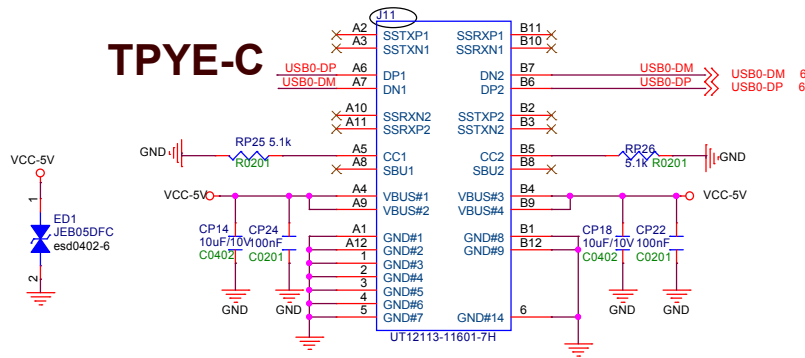
Xunlong Software Co., Limited		
Design Name ORANGEPI Zero3-4G		
Size A3	Page Name SOC1	Rev 1.0
Date: Thursday, July 21, 2022 Sheet 5 of 11		



DDR3

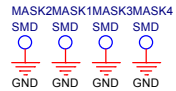
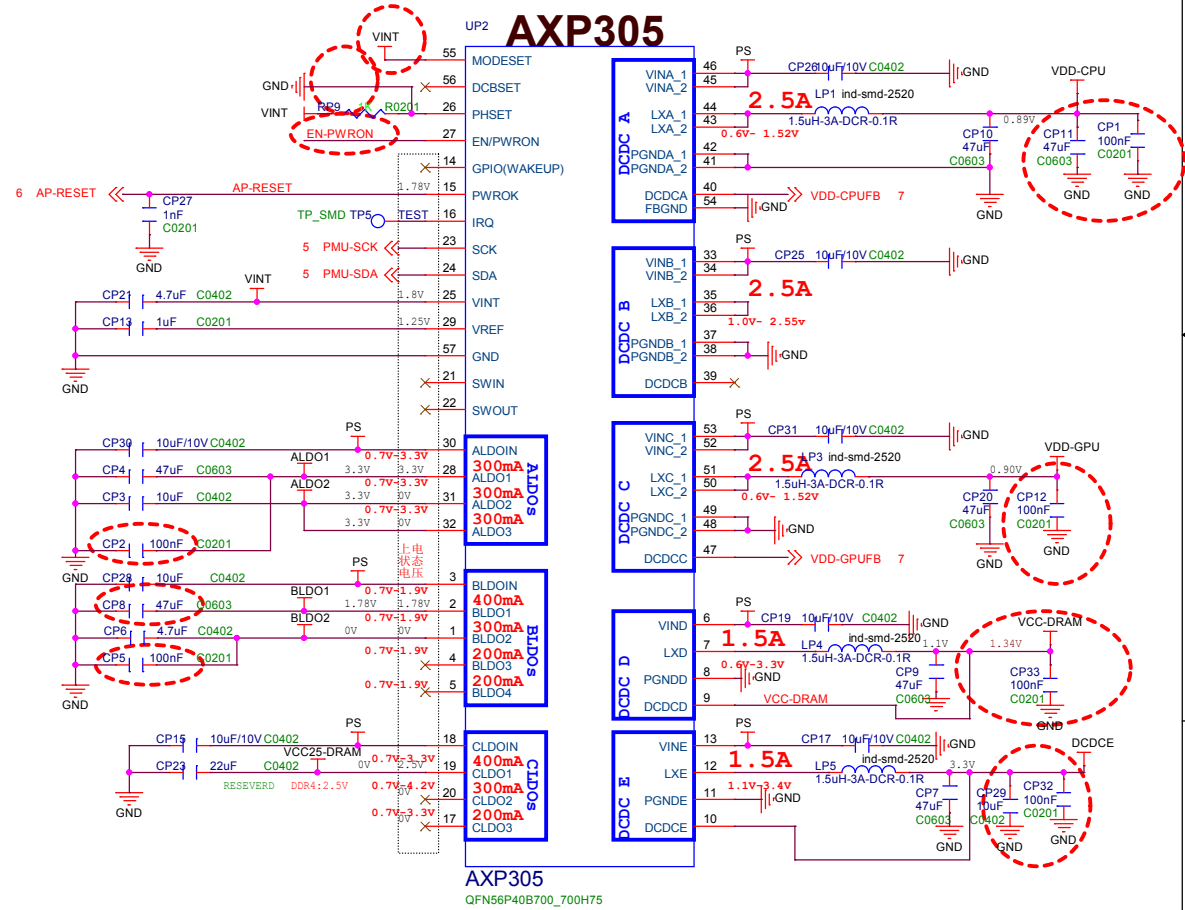


TPYE-C



PMIC

AXP305

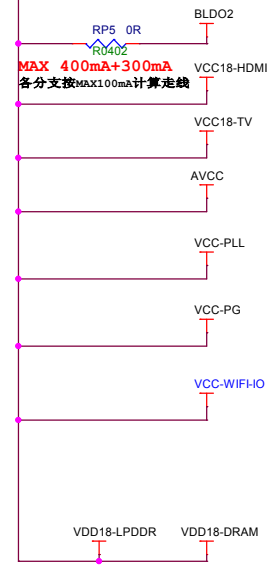


VDD-CPU MAX 2500mA

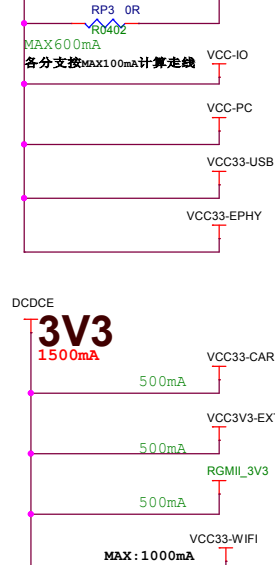
VCC-5V PS

VDD-GPU MAX 2500mA VDD-SYS

1V8



3V3



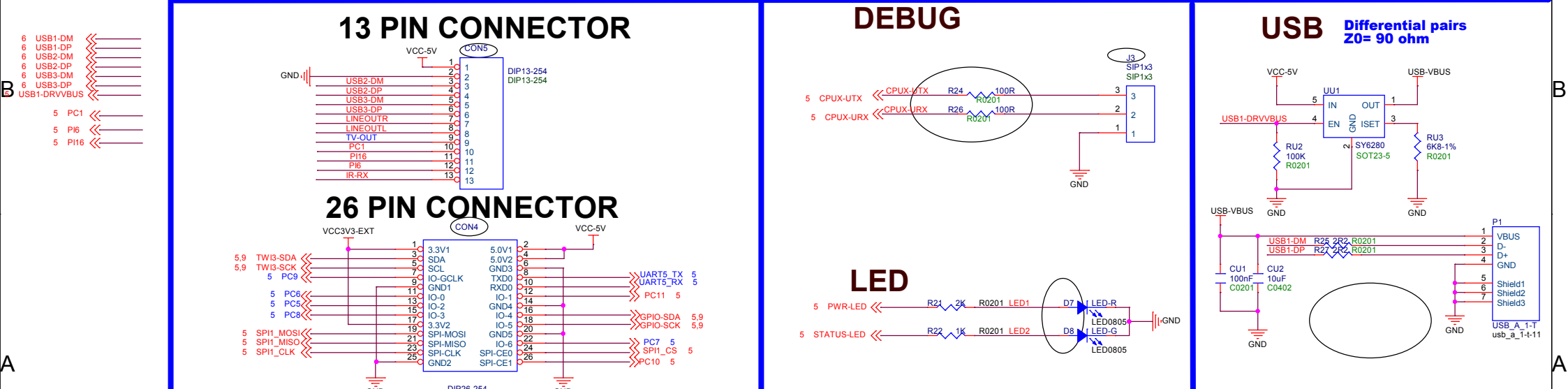
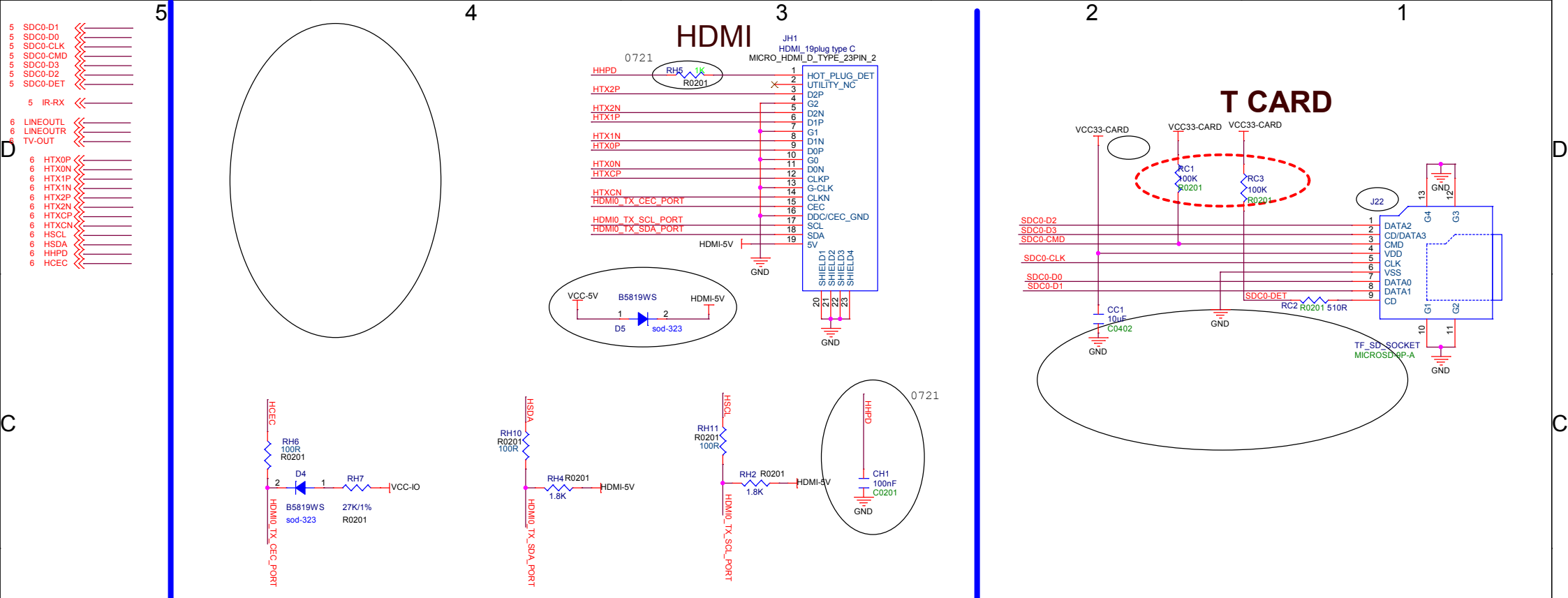
3V3

1500mA

MAX: 1000mA



Xunlong Software Co., Limited		
Design Name		
ORANGEPI Zero3-4G		
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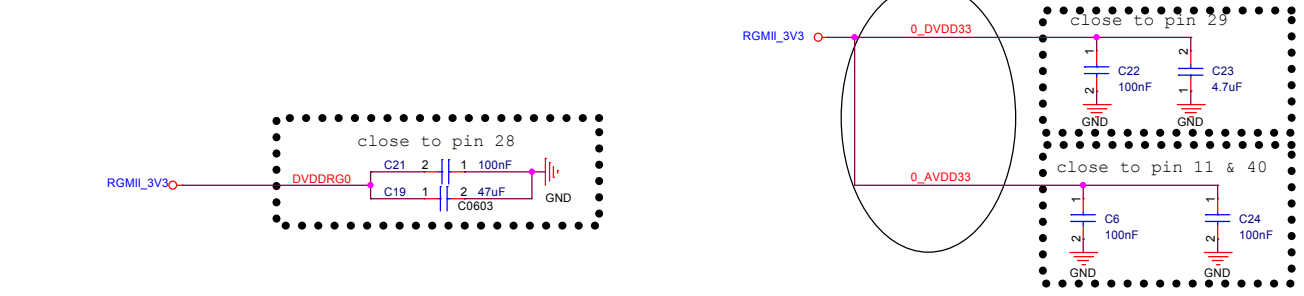
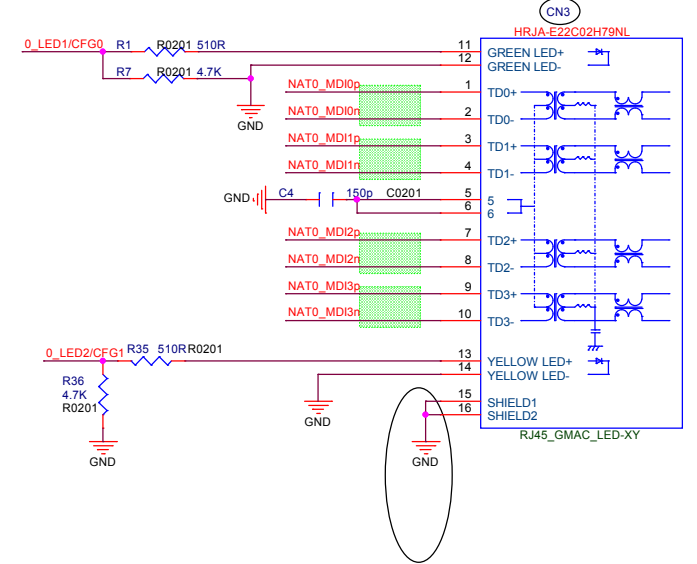
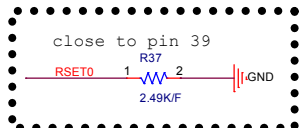
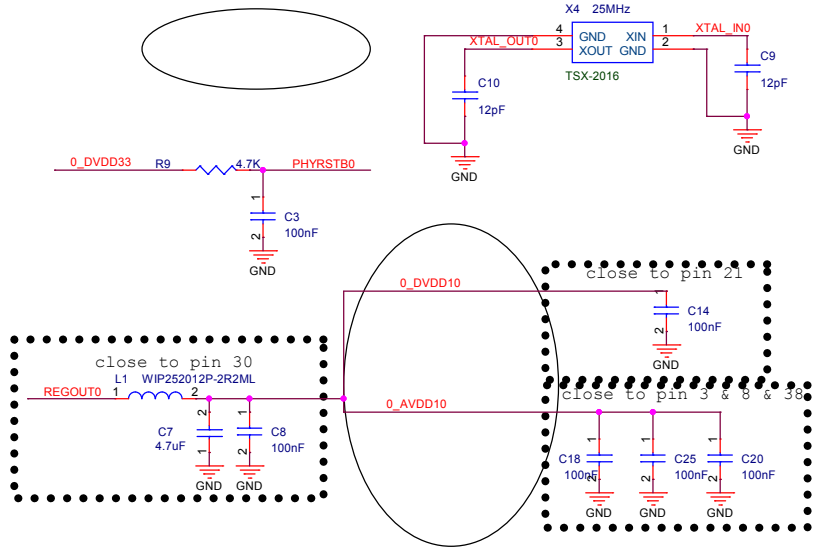
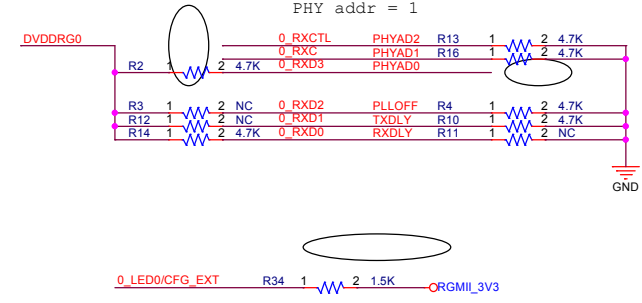
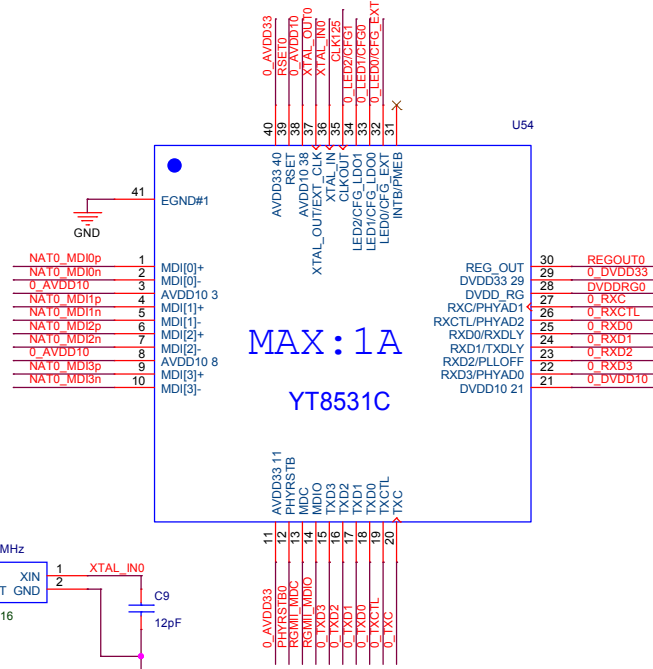
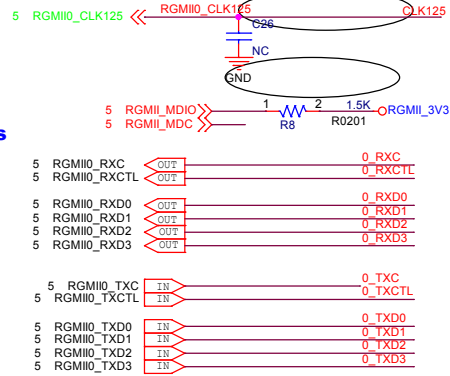


1000M ETH

Differential pairs
Z0= 100 ohm

To #616

From #616



WIFI+BT

