

Version List			
Version	Change List	Date	Author
1.0.0	First Release	2021-11-11	Miller.Zhang
1.0.0	1、C39改为22uF/0603	2021-11-11	Miller.Zhang



Title

01_Cover

Size

Document Number

CV180xB_QFN68_SIP_DDR

Rev

1.0.0

Date:

Wednesday, March 01, 2023

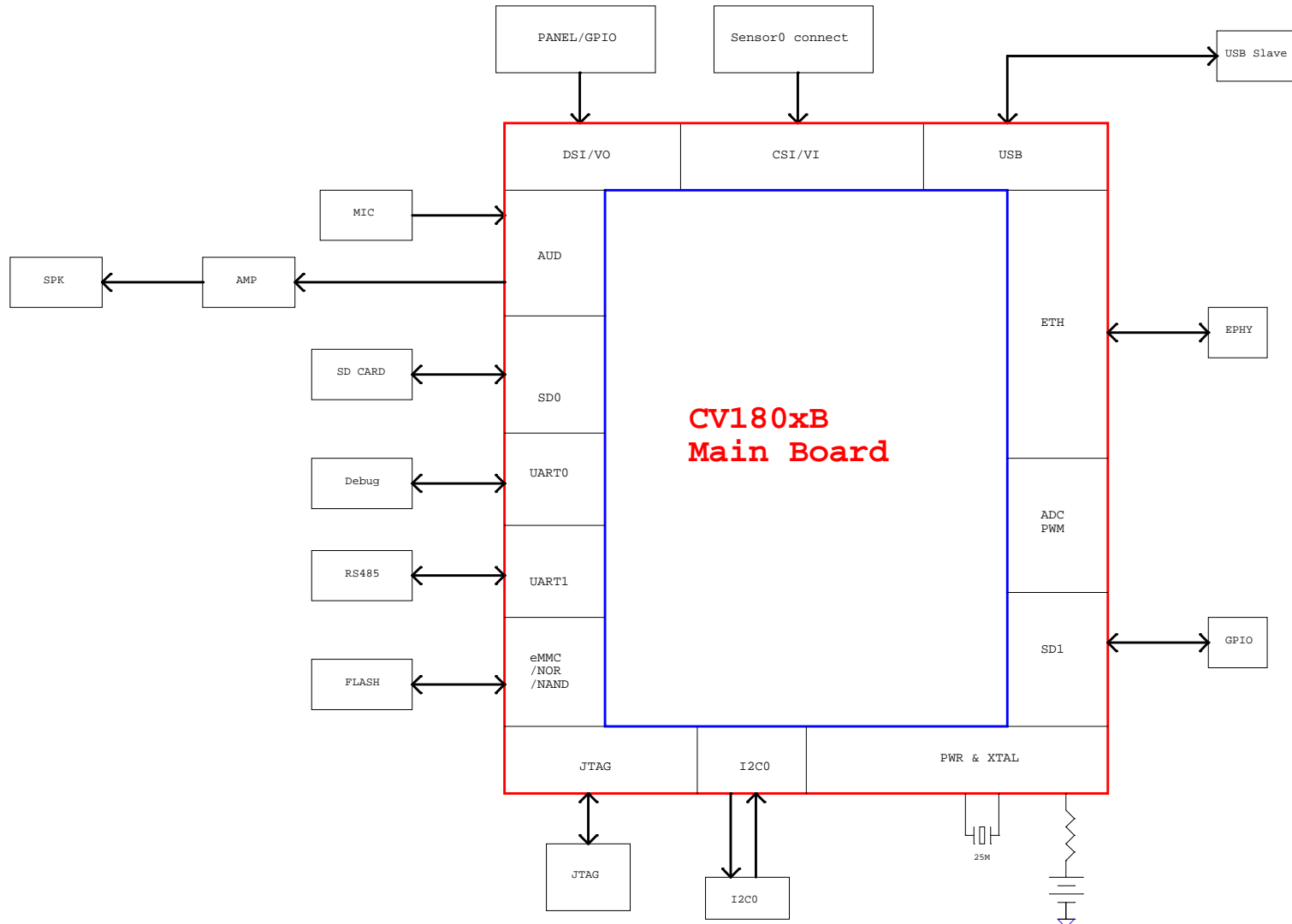
Sheet

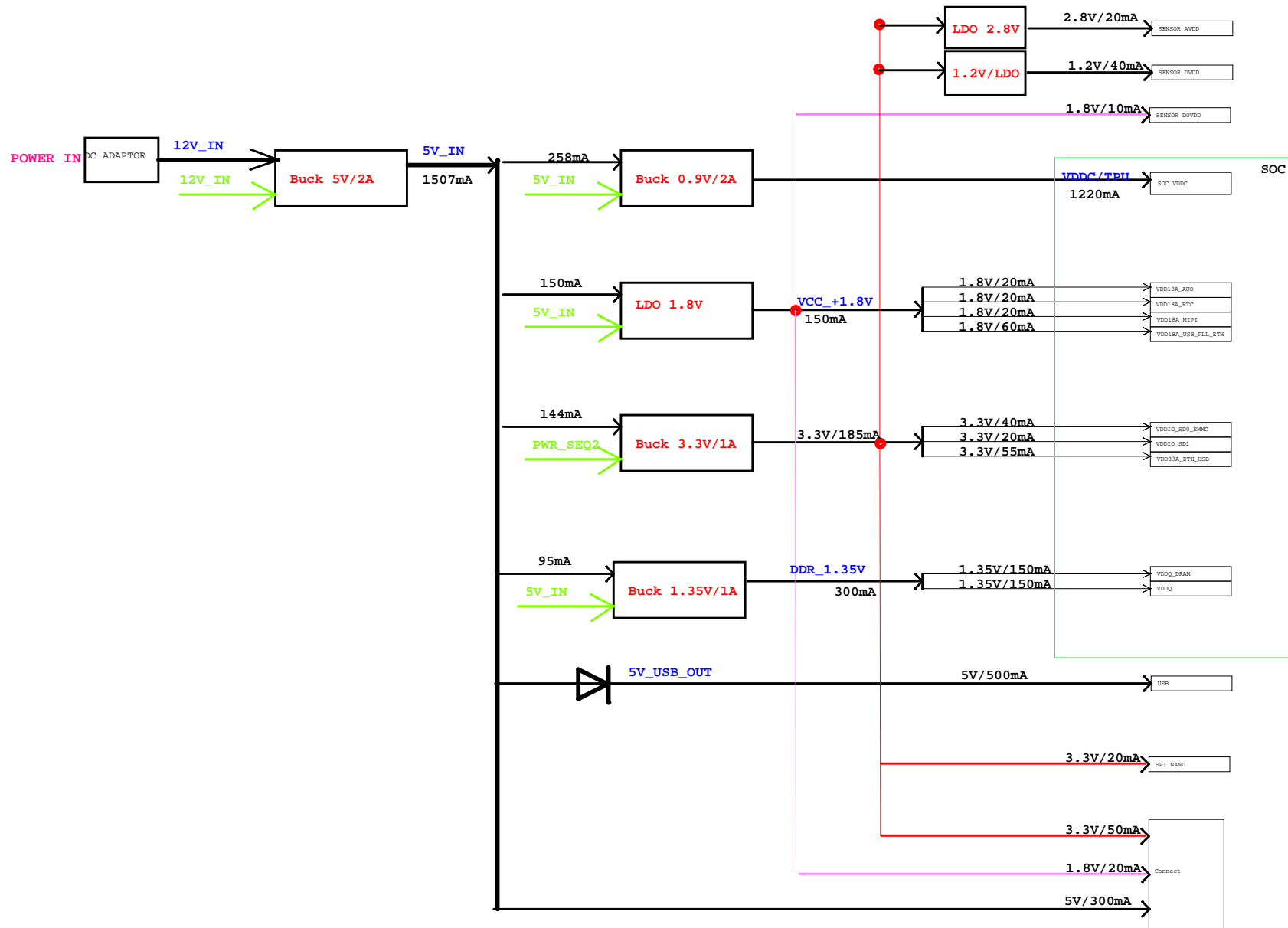
1

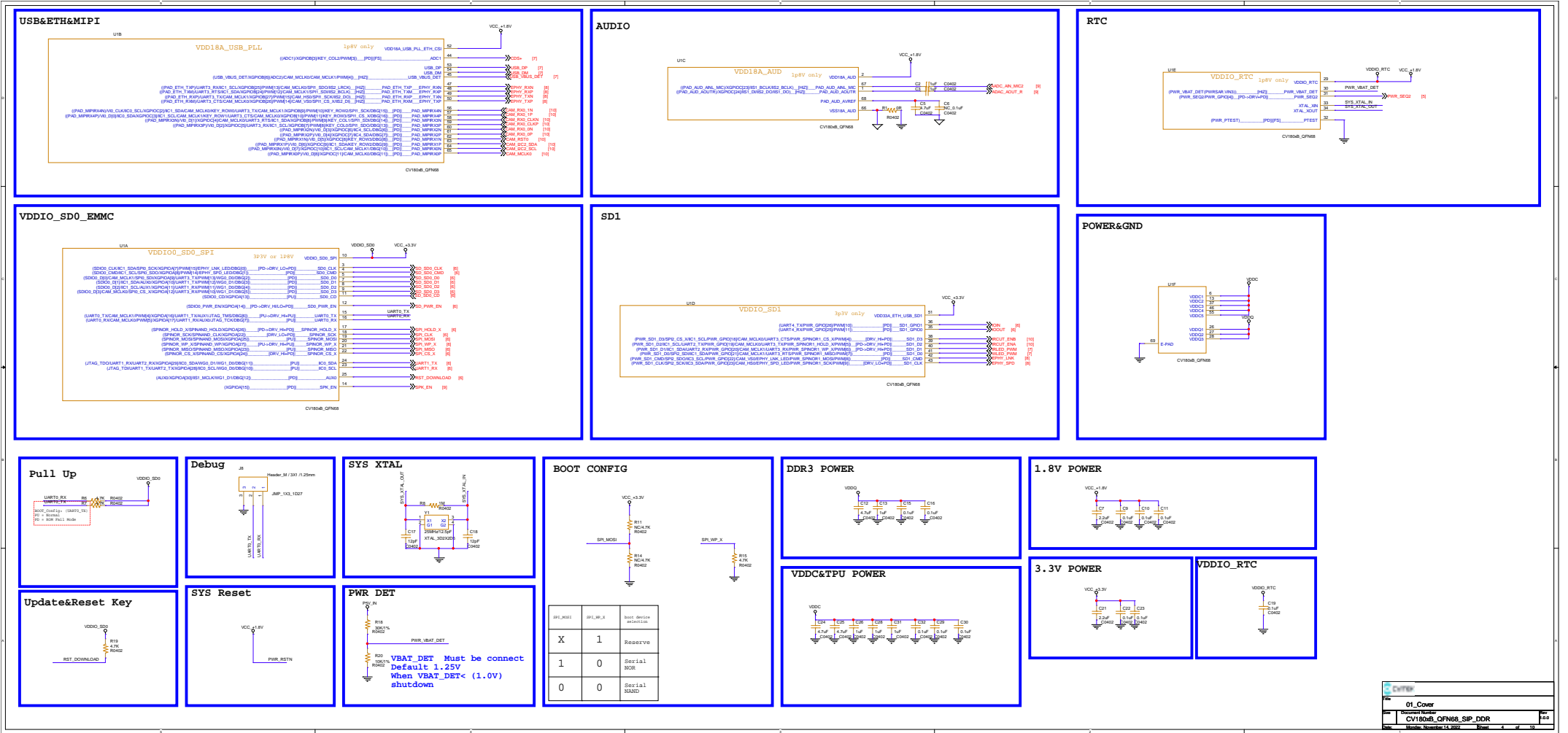
of

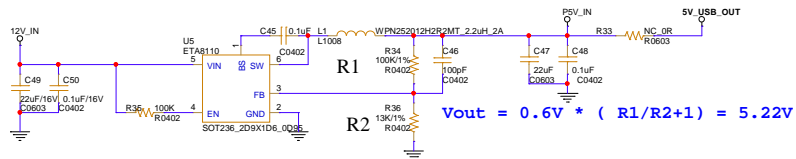
10

Block Diagram for CV180xB

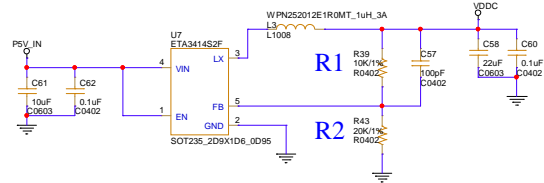
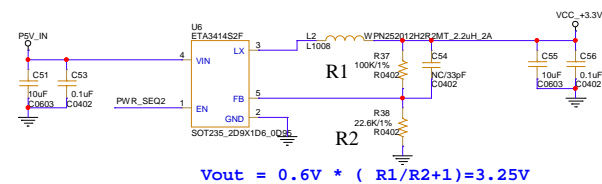






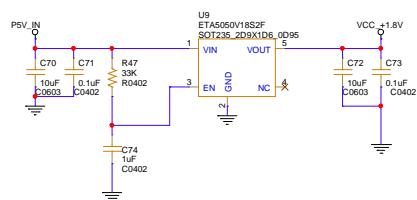
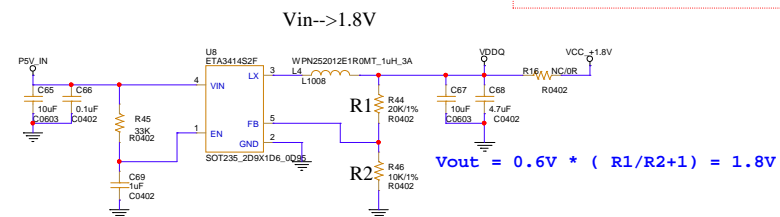


PWR_SEQ02



$R2 \approx 20k\Omega$ for stability consideration.

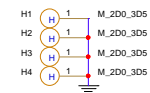
Note:
CV1801B/CV1802B: DDR3L
Vout=1.35V
CV1800B/CV1802B: DDR2L
Vout=1.8V



Note:
CV1801B/CV1802B:
U9,C70,C71,R47,C74,C72,C73 上件
R17 NC

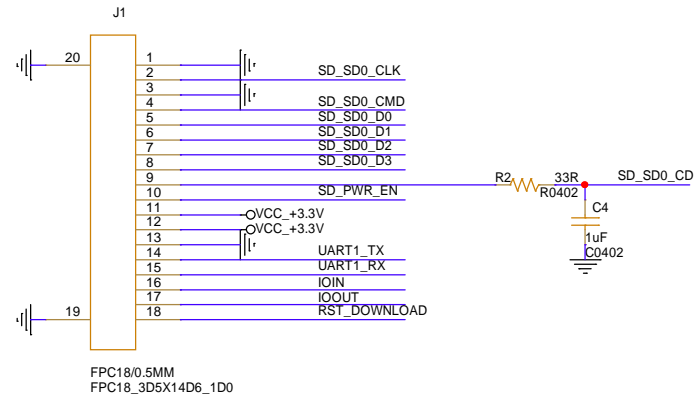
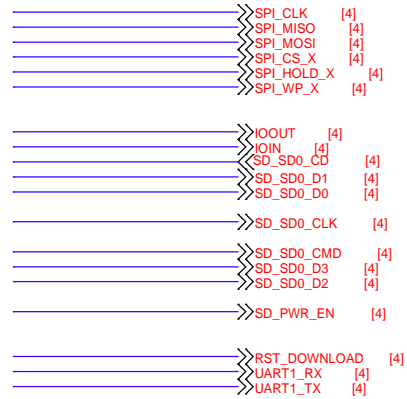
CV1800B/CV1802B:
U9,C70,C71,R47,C74,C72,C73 NC
R17 上件

→PWR_SEQ02 [4]

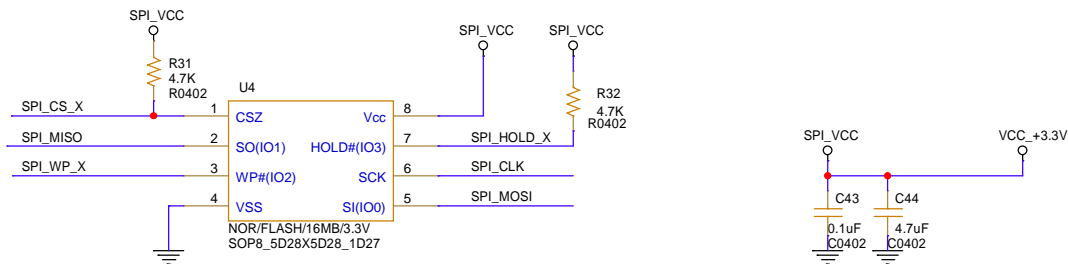


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NET



SPINOR

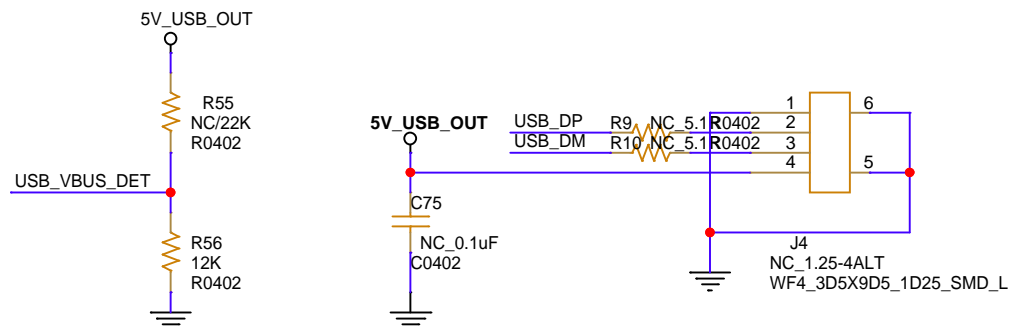


CVITEK		
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NET

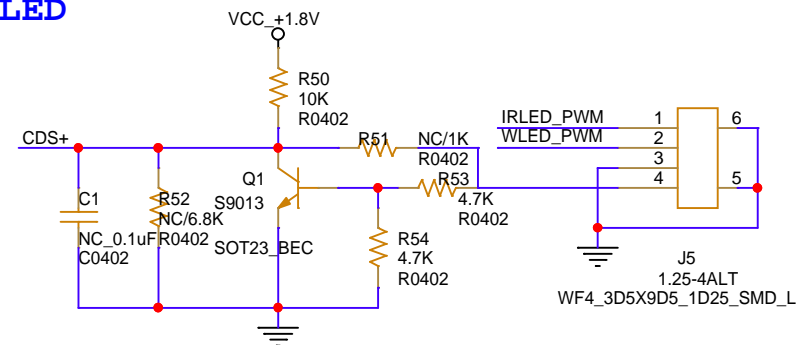


USB



VBUS_DET: (Host or device)
 LOW---- USB Host
 High---- USB Device

LED



Title

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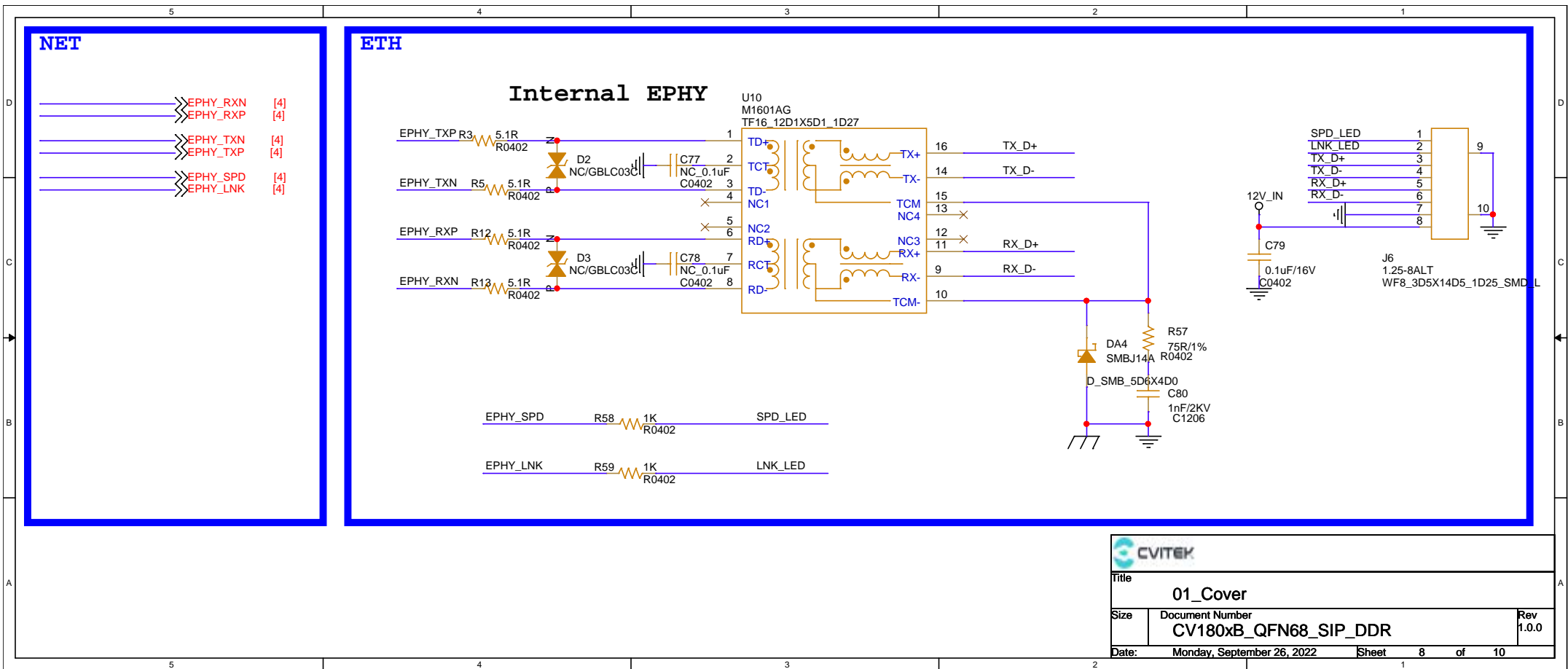
Monday, September 26, 2022

Sheet

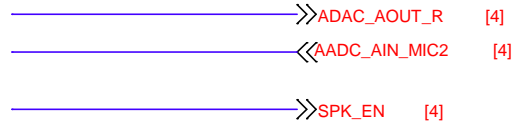
7

of

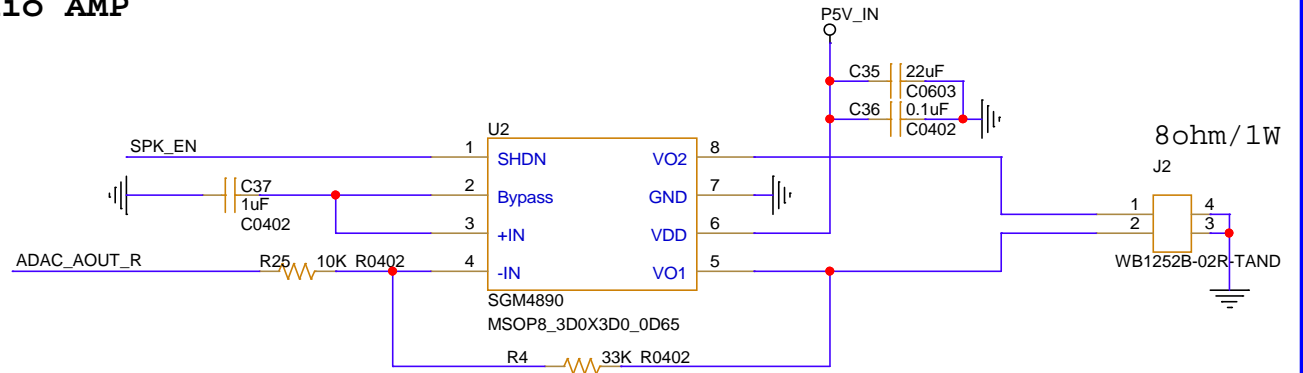
10



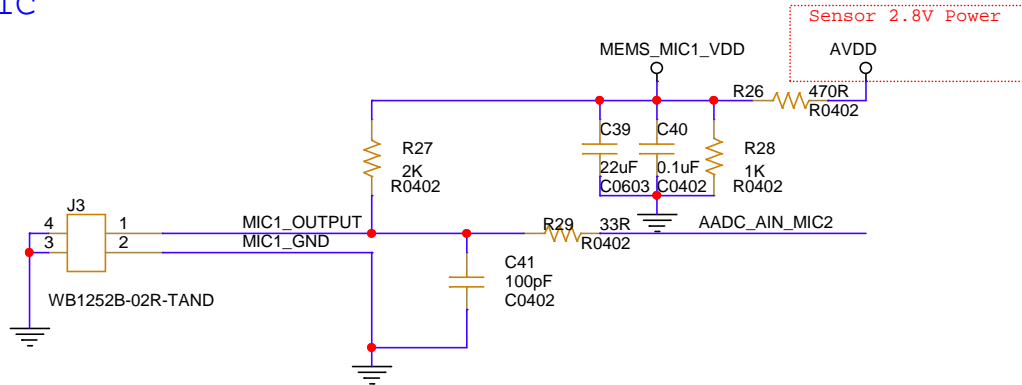
NET



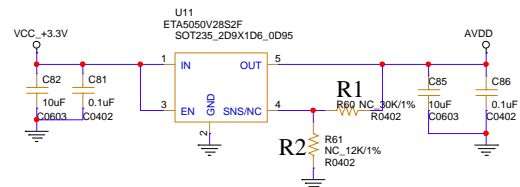
Audio AMP



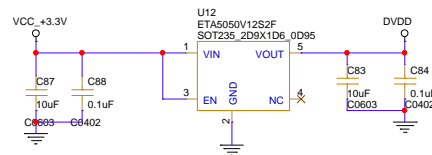
MIC



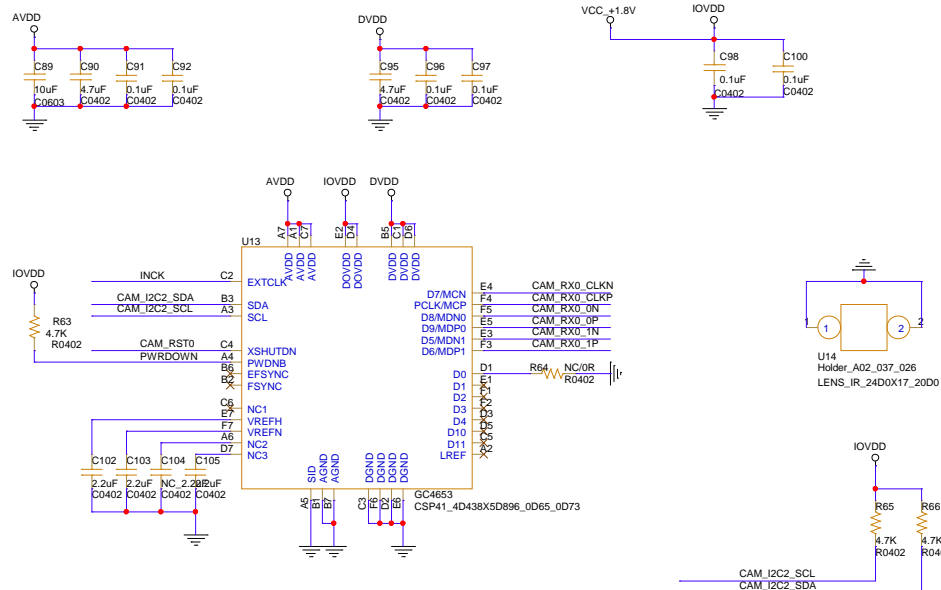
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$$V_{out} = 0.8V * (R1/R2 + 1)$$



SENSOR

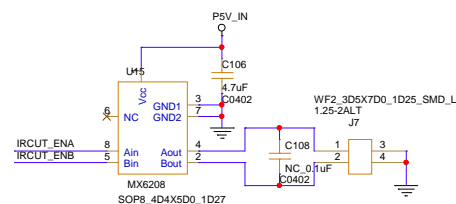


$$F_{inck} = 37.125\text{MHz}, 74.25\text{MHz}$$

NET

CAM_RX0_CLKN [4]
 CAM_RX0_CLKP [4]
 CAM_RX0_1N [4]
 CAM_RX0_1P [4]
 CAM_RX0_0N [4]
 CAM_RX0_0P [4]
 CAM_MCLK0 [4]
 CAM_RST0 [4]
 CAM_I2C2_SCL [4]
 CAM_I2C2_SDA [4]
 WRCUT_ENA [4]
 WRCUT_ENB [4]

IR CUT



PAD	SC200AI	SC3335	SC500AI	GC2063	GC4653
A6	C104	NC	NC	2.2uF	NC
D1	R64	NC	NC	NC	NC
D7	C105	NC	NC	2.2uF	2.2uF
MIP1	2Lan	2Lan	4Lan	2Lan	2Lan



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